

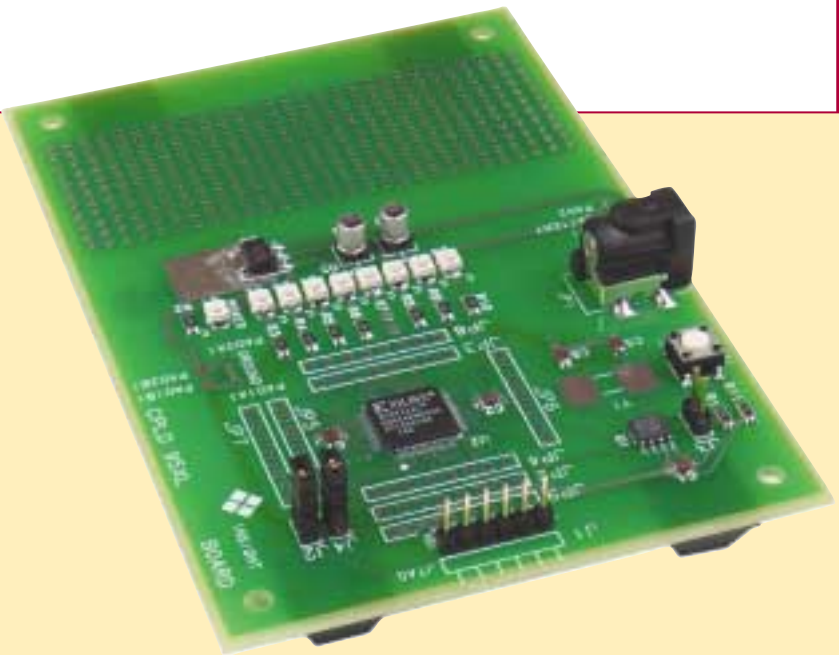
XC9500XL Development Kit

Product Brief

The XC9500XL Development Kit is a low cost, easy to use tool for designing XC9500XL applications.

XC9500XL Development Kit Includes:

- XC9500XL Development Board
 - XC9572XL-10VQ64 CPLD
 - JTAG programming port
 - 3.3 V regulator
 - Eight user LEDs
 - Oscillator
 - Large prototype area
- AC-to-DC power supply (Americas only)
- Application notes



Product Description

The XC9500XL Development Kit delivers an easy to use prototyping platform for the 3.3 V XC9500XL CPLD family. The XC9500XL family is targeted at high-performance, low voltage applications needing from 32 to 288 macrocells of in-system programmable logic. With enhanced pin-locking capability, the XC9500XL architecture addresses the demands of in-system programmability.

The board included in the XC9500XL Development Kit contains the 72 macrocell, 3.3 V XC9572XL device. Fifty-two user I/Os are available with

the VQ64 package used. Each of these signals is brought out to header strips surrounding the CPLD, allowing for easy prototyping and expansion. A DS1073 Econoscillator provides a clock source for the board, while a TI 3.3 V regulator, Infineon LEDs, a JTAG interface and a large prototype area complete the platform design. Power can be supplied directly or through the on-board regulator.

Development software to create designs for the XC9500XL family is

available in the free downloadable Xilinx® ISE WebPACK™. This software is provided on CD, along with a JTAG programming cable, in the WebPACK version of the XC9500XL kit. This combination of development board, software, and programming cable make the XC9500XL Development Kit the perfect choice for CPLD design applications.

Ordering Information

	Americas Part #	International Part #
XC9500XL Development Kit		
XC9500XL Development Kit	DS-KIT-95XL	DS-KIT-95XL-EURO
Kit with WebPACK CD and JTAG Cable	DS-KIT-95XL-PAK	DS-KIT-95XL-PAK-EURO